

ABSTRACT OF THE DISCLOSURE

A method of selecting CAS latencies in a system. Specifically, a system which includes a plurality of memory modules is provided. Because different memory modules may have different CAS latencies, multilevel signaling is used to standardize the CAS latencies throughout the system. A static pin is provided on each memory device such that a drive signal can be received. The voltage level of the drive signal corresponds to a selected CAS latency for the system.